

承认书

SPECIFICATION FOR APPROVAL

客户名称:

CUSTOMER:

产品名称:

PARTNAME:

产品规格:

SPECIFICATION:

承认书编号:

APPROVAL SHEET NO.:

发出日期:

ISSUED DATE:

多层片式瓷介电容器

Multi-Layer Ceramic Capacitors

TCC0603COG330F500CT

TCC0603COG330F500CT

DRAAW108N/3

DRAAW108N/3

2026/08/28

2026/08/28

制造 MANUFACTURER

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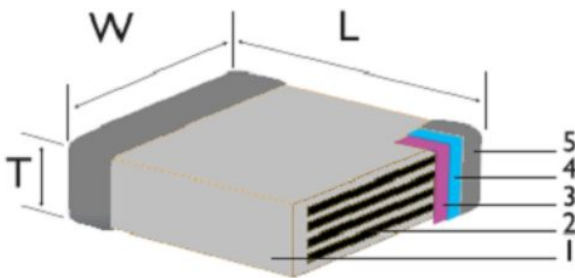
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1. 电容器及介质分类 Types of Capacitor and Dielectric Material

※COG（或称为NP0）：此类介质材料的电容器为 I 类 Class I 电容器，包括常规、中高压COG(NP0)产品。此类产品电性能稳定，几乎不随温度、电压和时间的变化而变化。适用于低损耗，稳定性要求高的电路中，如滤波器、谐振器和计时电路中。

※COG (or NP0) : The capacitor of this kind dielectric material is considered as Class I capacitor, including general capacitor and high frequency COG(NP0) capacitor. The electrical properties of COG(NP0) capacitor are the most stable one and have little change with temperature, voltage and time. They are suited for applications where low-losses and high-stability are required, such as filters, oscillators, and timing circuits.

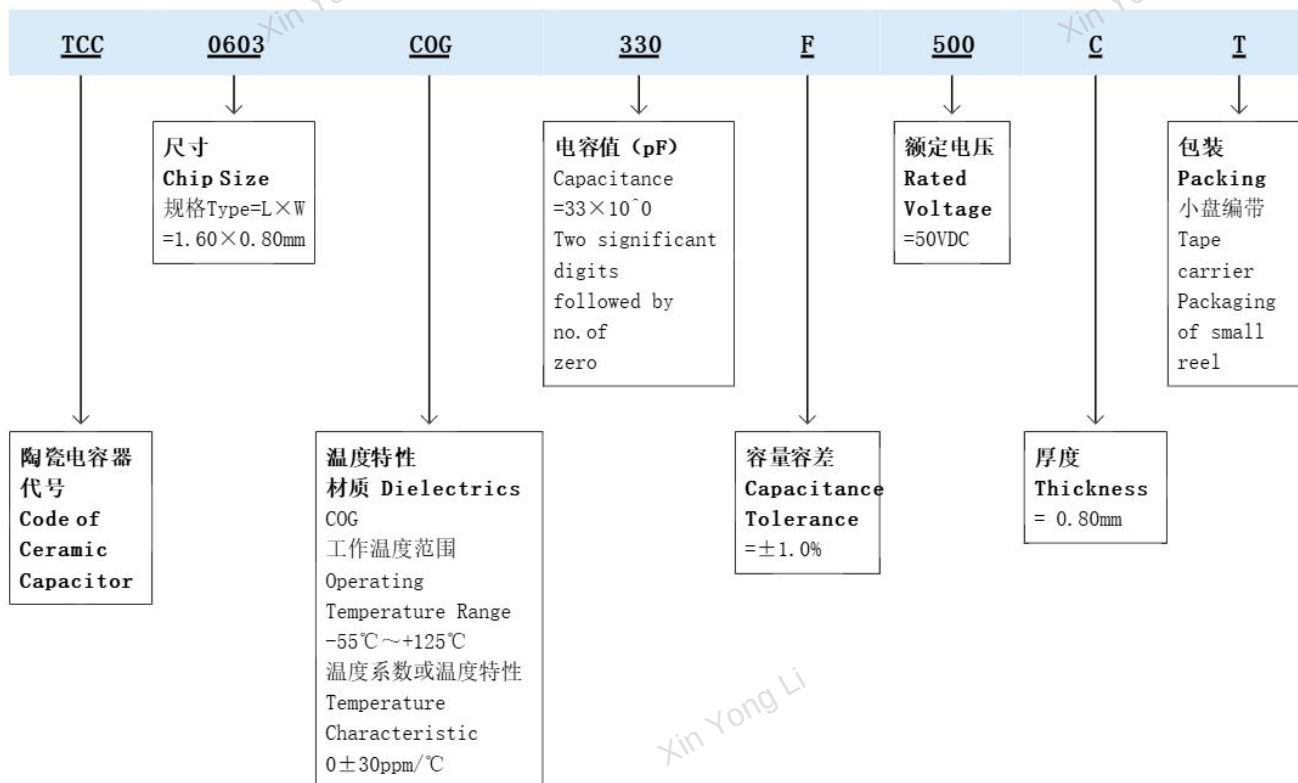
2. 产品结构 Product Frame



序号 No.	名称 Name
1	陶瓷介质 Ceramic dielectric
2	内电极 Inner electrode
3	外电极 Outer electrode
4	镍层 Nickel
5	锡层 Tin

3. 产品规格型号命名规则 General Product Parts Numbering System

(例) (Example)



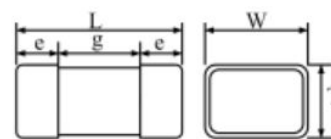
注意：选型时建议使用温度降额 20℃ 以上，使用电压降额 30% 以上。

Attention: It is recommended to decrease the operating temperature by 20℃ or more and decrease the operating voltage by 30% or more.

4. 产品尺寸 Dimensions

规格：0603

Chip Size: 0603



规格	L	W	e	g min	T
Type	(mm)	(mm)	(mm)	(mm)	(mm)
0603	1.60±0.10	0.80±0.10	0.2~0.6	0.50	0.80±0.10

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5. 技术要求和测试条件 Specification and Testing Conditions

5.1 容量 Capacitance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	在要求的容值容差范围内 Within the specified tolerance 32.67~33.33pF	1.0±0.2Vrms, 1MHz±10%
备注：测试温度：25℃±3℃，测试湿度：<70%RH。针对二类介质规格需去老化处理，条件：电容器在150℃热处理1小时，放置48小时后进行测量。 参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.5.1 II类 Class II GB-T 21042-2007 4.5.1		

5.2 损耗 Dissipation Factor

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	DF≤0.1%	1.0±0.2Vrms, 1MHz±10%
备注：测试温度：25℃±3℃，测试湿度：<70%RH。针对二类介质规格需去老化处理，条件：电容器在150℃热处理1小时，放置48小时后进行测量。 参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.5.2 II类 Class II GB-T 21042-2007 4.5.2		

5.3 绝缘电阻 Insulation Resistance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	≥10GΩ	测试电压：50V 充电时间：60±5秒 充放电电流：≤50mA Test voltage: 50V Charge Time: 60±5sec Charge/discharge current: 50mA max.
备注：测试温度：25℃±3℃，测试湿度：<70%RH。 参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.5.3 II类 Class II GB-T 21042-2007 4.5.3		

5.4 尺寸 Dimensions

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	在要求的范围内 Within the specified dimensions	用千分尺 Using calipers on micrometer
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.4 II类 Class II GB-T 21042-2007 4.4		

5.5 耐电压 Voltage Proof

类型 Dielectrics	额定电压范围 Rated voltage range	耐电压性能测试方法 Measuring Method
COG	$U_r=50V$	施加电压150VDC, 5秒, 最大电流不超过50mA。 Force 150VDC for 5s. Max.current should not exceed 50 mA.
测试温度: $25^{\circ}\text{C} \pm 3^{\circ}\text{C}$, 测试湿度: $<70\%\text{RH}$ 。 参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.5.4 II类 Class II GB-T 21042-2007 4.5.4		

注意：耐电压测试的电压为瞬间电压。

Attention: The Voltage Proof test uses transient voltage.

5.6 外观 Appearance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	$l \leq 1/8L$, $w \leq 1/8W$, $t \leq 1/8T$ （任意一项不符合，均判定不合格） (If any one is unacceptable, all judged unqualified)	目视检查 Visual inspection.
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.4 II类 Class II GB-T 21042-2007 4.4		

5.7 可焊性 Solderability

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	端电极挂锡面积不小于95%，针孔或粗糙面积小于5% 95% min-coverage of both terminal electrodes and less than 5% have pin holes or rough spots	锡炉温度: $245 \pm 5^{\circ}\text{C}$ 浸入时间: 2 ± 1 秒 两侧端电极完全浸入焊锡炉 Solder temperature: $245 \pm 5^{\circ}\text{C}$ Dipping time: 2 ± 1 seconds Completely soak both terminal electrodes in solder
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.10 II类 Class II GB-T 21042-2007 4.10		

5.8 耐焊性 Resistanceto leaching

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	端电极挂锡面积不小于95%，针孔或粗糙面积小于5%，外观无开裂 95% min. coverage of both terminal electrodes and less than 5% have pin holes or rough spots.No obvious damage on appearance	预热: $120^{\circ}\text{C} \sim 150^{\circ}\text{C}/60$ 秒 锡炉温度: $270 \pm 5^{\circ}\text{C}$ 浸入时间: 10 ± 1 秒 两侧端电极完全浸入焊锡炉 Preheating: $120^{\circ}\text{C} \sim 150^{\circ}\text{C}/60\text{sec}$ Solder temperature: $270 \pm 5^{\circ}\text{C}$ Dipping time: 10 ± 1 seconds Completely soak both terminal electrodes in solder
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.9 II类 Class II GB-T 21042-2007 4.9		

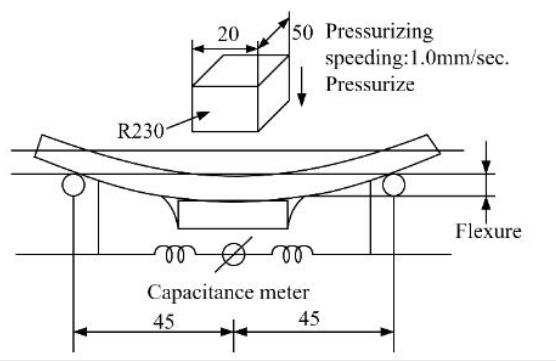
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E-mail: info@cctc.cc

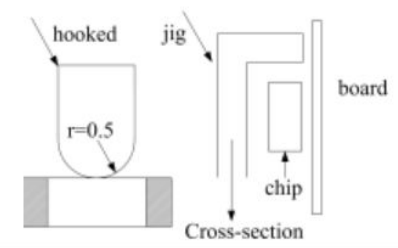
传真 (Fax) : (86)-0768-6855921, 6855928

Website <https://www.cctc.cc>

5.9 端电极结合强度 Board Flex

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	外观无明显可见损伤 容量变化在±5%以内 No obvious damage on appearance Cp change within ±5%	将片状电容器安装在测试夹具上，按图所示方向以1.0mm/s的速率施加压力，弯曲1.0mm，保持5±1s。 Solder the capacitor on testing substrate and put it on testing stand. The middle part of substrate shall successively be pressurized by pressuring rod at a rate of about 1.0mm/sec. Until the deflection become means of the 1.0mm, and hold for 5±1s.  Pressurizing speeding:1.0mm/sec. Pressurize Flexure Capacitance meter 45 45
备注：0603规格的测试板厚度为1.6mm 参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.8 II类 Class II GB-T 21042-2007 4.8		

5.10 附着力 Terminal Strength

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	端电极无松动，也无其它不良现象 No removal of the terminations or other defect shall occur	施加6N的压力，并保持10±1秒 The pressurizing force shall be 6N and the duration of application shall be 10±1sec.  hooked jig r=0.5 board chip Cross-section
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.7 II类 Class II GB-T 21042-2007 4.7		

5.11 耐焊接热 Resistanceto SolderingHeat

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	外观无明显可见损伤 容量变化在 $\pm 2.5\%$ 以内 DF满足产品初始值的要求 IR满足产品初始值的要求 No obvious damage on appearance Cp change within $\pm 2.5\%$ DF and IR meets initial standard value	焊接温度： $270 \pm 5^\circ\text{C}$ 预热： $120 \sim 150^\circ\text{C}$ ，60秒 浸入时间： 10 ± 1 秒 实验后在室温下放置 24 ± 2 小时，然后测量 Soldering temperature: $270 \pm 5^\circ\text{C}$ Preheating: $120 \sim 150^\circ\text{C}$, 60sec Dipping time: 10 ± 1 seconds Post-experimental, left at room temperature for 24 ± 2 hrs, then measure.
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.9 II类 Class II GB-T 21042-2007 4.9		

5.12 静电容量温度特性 TemperatureCharacteristicsof Capacitance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition												
COG	容量变化在±30ppm/℃以内 容量漂移在±0.2%以内 Cp change within ±30ppm/℃ Cp drift within ±0.2%	按COG系列温度顺序测试电容容量： Measure capacitance under follow table list temperature(℃)： 在140～150℃预处理1小时，放置24小时后进行测量，测试电压≤1Vrms。 Heat at 140～150℃ for 1 hour, Leave at room temperature for 24hrs and then measure, The test voltage is 1Vrms max. <table><tr><th>步骤 Step</th><th>温度 Temp (℃)</th></tr><tr><td>1</td><td>25±2</td></tr><tr><td>2</td><td>-55±3</td></tr><tr><td>3</td><td>25±2</td></tr><tr><td>4</td><td>125±3</td></tr><tr><td>5</td><td>25±2</td></tr></table> COG 容量漂移的计算方法为：用步骤3测量的电容容值除以在第1、3和5步骤测量得到的误差的最大值和最小值的差。温度系数的计算以第3步骤测量电容容量为参考。 The capacity drift is calculated by dividing the capacitance capacity measured in step 3 by the difference between the maximum and minimum errors measured in steps 1, 3, and 5. The calculation of the temperature coefficient is based on the capacitance measurement in step 3.	步骤 Step	温度 Temp (℃)	1	25±2	2	-55±3	3	25±2	4	125±3	5	25±2
步骤 Step	温度 Temp (℃)													
1	25±2													
2	-55±3													
3	25±2													
4	125±3													
5	25±2													
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.6 II类 Class II GB-T 21042-2007 4.6														

5.13 温度快速循环 TemperatureCycling

类型 Dielectrics	技术要求 Specification	测试条件 TestingCondition															
COG	外观无明显可见损伤 容量变化在±2.5%以内 No obvious damage on appearance Cp change within ±2.5%	按下列步骤进行5次循环： To perform 5 cycles of the stated environment: <table border="1"> <thead> <tr> <th>步骤 Step</th><th>温度 Temperature</th><th>时间 Time</th></tr> </thead> <tbody> <tr> <td>1</td><td>下限类别温度+0/-3℃ Min.operating Temp.+0/-3℃</td><td>30min</td></tr> <tr> <td>2</td><td>25℃</td><td>1 min</td></tr> <tr> <td>3</td><td>上限类别温度+3/-0℃ Max.operating Temp.+3/-0℃</td><td>30 min</td></tr> <tr> <td>4</td><td>25℃</td><td>1min</td></tr> </tbody> </table> 实验后在室温下放置 24±2小时，然后测量 Measurement to be made after being kept at room temperature for 24±2hrs at room temperature, then measure.	步骤 Step	温度 Temperature	时间 Time	1	下限类别温度+0/-3℃ Min.operating Temp.+0/-3℃	30min	2	25℃	1 min	3	上限类别温度+3/-0℃ Max.operating Temp.+3/-0℃	30 min	4	25℃	1min
步骤 Step	温度 Temperature	时间 Time															
1	下限类别温度+0/-3℃ Min.operating Temp.+0/-3℃	30min															
2	25℃	1 min															
3	上限类别温度+3/-0℃ Max.operating Temp.+3/-0℃	30 min															
4	25℃	1min															
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.11 II类 Class II GB-T 21042-2007 4.11																	

5.14 稳态湿热 MoistureResistance

类型 Dielectrics	技术要求 Specification	测试条件 TestingCondition
COG	外观无明显可见损伤 容量变化在±5%以内 DF≤0.29% IR≥1000MΩ No obvious damage on appearance Cp change within ±5% DF≤0.29% IR≥1000MΩ	测试温度：40±2℃ 湿度：90~95% RH 测试时间：500±12小时 实验后在室温下放置 24±2小时，然后测量 Test temperature: 40±2℃ Humidity: 90~95% RH Testing time: 500±12hrs Post-experimental treatment, left at room temperature for 24±2hrs, then measure.
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.13 II类 Class II GB-T 21042-2007 4.13		

5.15 耐湿负荷 Biased Humidity

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	外观无明显可见损伤 容量变化在 $\pm 7.5\%$ 以内 $DF \leq 0.50\%$ $IR \geq 500M\Omega$ No obvious damage on appearance Cp change within $\pm 7.5\%$ $DF \leq 0.50\%$ $IR \geq 500M\Omega$	测试温度： $40 \pm 2^\circ C$ 湿度：90~95% RH 电压：额定电压 测试时间： 500 ± 12 小时 充放电电流： $\leq 50mA$ 实验后在室温下放置 24 ± 2 小时，然后测量 Test temperature: $40 \pm 2^\circ C$ Humidity: 90~95% RH Voltage: Rated voltage Testing time: 500 ± 12 hrs Charge/discharge current: 50mA max. Post-experimental treatment, left at room temperature for 24 ± 2 hrs, then measure.
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.18 II类 Class II GB-T 21042-2007 4.18		



5.16 耐久性 Durability

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
COG	外观无明显可见损伤 容量变化在 $\pm 3.0\%$ 以内 $DF \leq 0.29\%$ $IR \geq 1000M\Omega$ No obvious damage on appearance Cp change within $\pm 3.0\%$ $DF \leq 0.29\%$ $IR \geq 1000M\Omega$	测试温度：上限类别温度 $\pm 3^{\circ}C$ 电压：1.5倍额定电压 测试时间：1000小时 充放电电流： $\leq 50mA$ 实验后在室温下放置 24 ± 2 小时，然后测量 Test temperature: Max. Operating Temp. $\pm 3^{\circ}C$ Voltage: 150% of the rated voltage Testing time: 1000hrs Charge/discharge current: 50mA max. Post-experimental, left at room temperature for 24 ± 2 hrs, then measure.
参考标准 Reference standard: I类 Class I GB-T 21041-2007 4.14 II类 Class II GB-T 21042-2007 4.14		

6. 产品包装 Packing

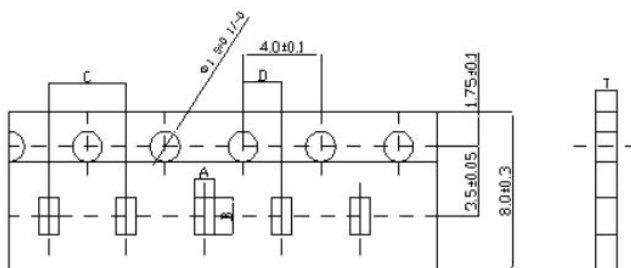
6.1 袋式散装 Bulk Packing

10000个/袋或按客户要求。Standard packing 10Kpcs/bag; others are according to customer request.

6.2 编带式包装 Tape Packing

规格	尺寸 Size (mm)			编带数量(个/盘 pcs/reel)	
Type	长度L	宽度W	厚度T	小盘纸带Small Paper Tape	小盘塑料带Small Plastic Tape
0603	1.60	0.80	0.80	4000	N/A

6.2.1 纸带尺寸 Dimensionsof PackingPaper

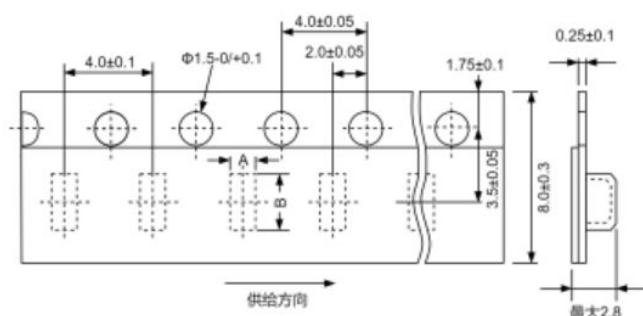


Type	A	B	C	D	T
0105	0.23±0.03	0.43±0.03	2.00±0.05	2.00±0.05	0.50max
0201	0.40±0.09	0.70±0.09	2.00±0.05	2.00±0.05	0.60max
0402	0.65±0.15	1.20±0.15	2.00±0.05	2.00±0.05	0.80max
0603	1.05±0.15	1.90±0.15	4.00±0.10	2.00±0.10	1.10max
0805	1.55±0.15	2.30±0.15	4.00±0.10	2.00±0.10	1.10max
1206	2.00±0.04	3.50±0.04	4.00±0.10	2.00±0.10	1.10max

(单位 unit:毫米 mm)

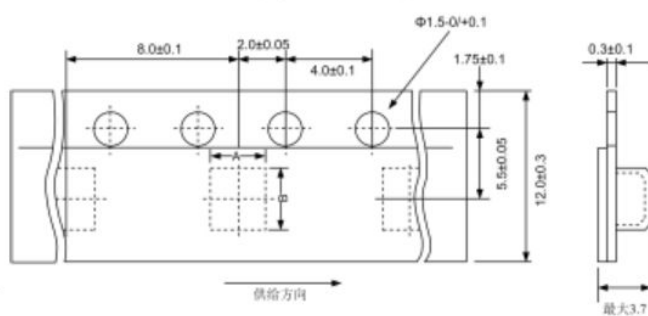
6.2.2 塑料带尺寸 Dimensionsof EmbossedPacking

8mm宽，4mm间距编带



A: 1.40±0.20 B: 2.25±0.20 (0805)
A: 1.90±0.20 B: 3.50±0.20 (1206)
A: 2.90±0.30 B: 3.60±0.30 (1210)

12mm宽，8mm间距编带



A: 3.60±0.20 B: 5.00±0.20 (1812)
A: 5.60±0.20 B: 6.10±0.20 (2220)

(单位 unit:毫米 mm)

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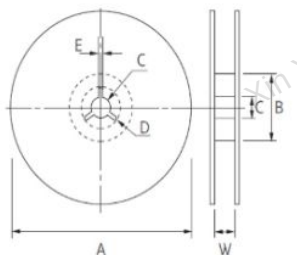
E-mail: info@cctc.cc

传真 (Fax) : (86)-0768-6855921, 6855928

Website <https://www.cctc.cc>

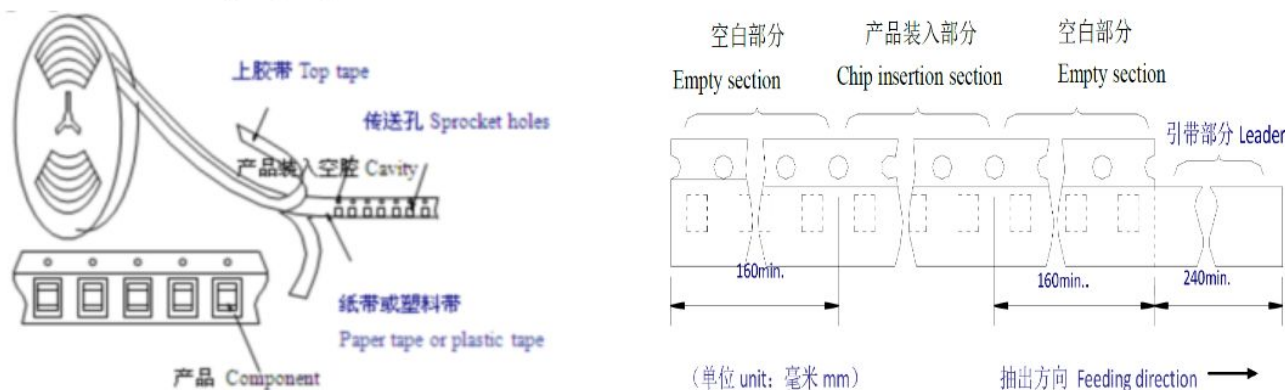
6.2.3 编带盘尺寸 Dimensionsof Reel

[unit: mm]



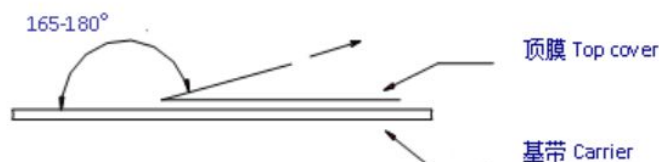
类型	载带宽度	A	B	C	D	E	W
7'' 盘	8mm	$\Phi 178 \pm 2.0$	MIN $\Phi 50$	$\Phi 13 \pm 0.5$	21 ± 0.8	2.0 ± 0.5	10 ± 1.5
	12mm	$\Phi 178 \pm 2.0$	MIN $\Phi 50$	$\Phi 13 \pm 0.5$	21 ± 0.8	2.0 ± 0.5	13 ± 0.5
13'' 盘	8mm	$\Phi 330 \pm 2.0$	MIN $\Phi 70$	$\Phi 13 \pm 0.5$	21 ± 0.8	2.0 ± 0.5	10 ± 1.5
	12mm	$\Phi 330 \pm 2.0$	MIN $\Phi 70$	$\Phi 13 \pm 0.5$	21 ± 0.8	2.0 ± 0.5	13 ± 0.5

6.2.4 编带方式 Taping Figure

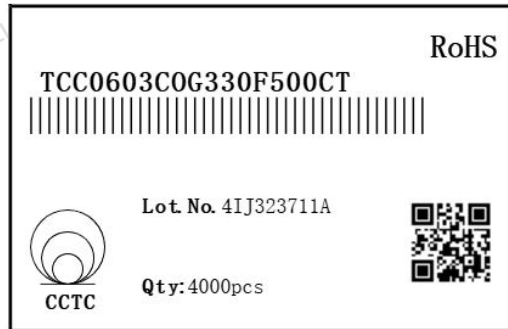


6.2.5 编带方法 Taping Method

- ①. 包装电容器的编带是顺时针卷绕的，由上往下的方向拉出编带时，传送孔处于编带的右侧。
 - ②. 在编带的前端，至少留出5个间距的引出带。
 - ③. 在编带时，必须按下图留出引带部分或空白部分。
 - ④. 上胶带和下胶带不应超出编带的边缘，不能挡住传送孔。
 - ⑤. 传送孔的累计误差为10个间距： ± 0.3 毫米以内。
 - ⑥. 上胶带的剥离力矩应在0.1至0.6牛顿以内，其方向如下图所示。
- ①. Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.
 - ②. The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.
 - ③. Part of the leader and part of the empty tape shall be attached to the end of the tape as follows.
 - ④. The top tape and bottom tape shall not protrude beyond the edges of the tape and shall not cover sprocket holes.
 - ⑤. Cumulative tolerance of sprocket holes, 10 pitches: ± 0.3 mm.
 - ⑥. Peeling off force: 0.1 to 0.6N in the direction shown down.



6.2.6 产品标签 Reel Label



(1) 标签内容 The Content of Label

<u>TCC</u>	<u>0603</u>	<u>COG</u>	<u>330</u>	<u>F</u>	<u>500</u>	<u>C</u>	<u>I</u>
①	②	③	④	⑤	⑥	⑦	⑧

①. 陶瓷电容器代号 Code of Ceramic Capacitor ②. 尺寸 chip size ③. 温度特性 dielectrics ④. 容量 capacitance
⑤. 容量公差 tolerance ⑥. 额定电压 rated voltage ⑦. 厚度 thickness ⑧. 包装 packing

(2) 产品批号 Lot No. :4IJ323711A

(3) 数量 Qty: 4000pcs

(4) RoHS GREEN PARTS 绿色物料

(5) CCTC 潮州三环的简称 (Simplified vendor name of ChaoZhou Three-circle)

6.2.7 外包装 Package

6.2.7.1 包装箱 Carton

6.2.7.1.1 包装箱尺寸 Carton Size

L	W	H
41.0±3cm	38.5±3cm	20.2±3cm

6.2.7.1.2 数量 Quantity

240Kpcs / 箱

1 内包装盒=40000PCS

1 包装箱=40000PCS × 6内包装盒=240KPCS

RoHS标识(根据客户要求张贴)

The Quantity:240Kpcs /per carton

1 INNER BOX=40000PCS

1 CARTON=40000PCS × 6 INNER BOX=240KPCS

according to customer request

6.2.7.2 内包装盒 InnerBox

6.2.7.2.1 包装盒尺寸 Size

L	W	H
18.0±1cm	18.5±1cm	11.8±1cm

6.2.7.2.2 数量 Quantity

40000pcs / 内包装盒

1盘=4000PCS

1 内包装盒=4000PCS × 10盘=40000PCS

40000pcs / INNER BOX

1 REEL=4000PCS

1 INNER BOX=4000PCS × 10 REEL=40000PCS

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7. MLCC使用注意事项 Precautions on the use of MLCC

7.1 电路板设计 PCB Design

7.1.1 电路板图案设计 Design of Land patterns

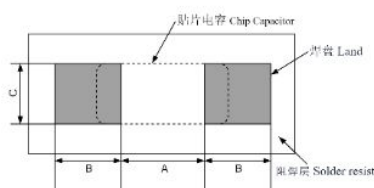
以下图片和表格给出了部分推荐的设计图案，可以防止安装时焊锡量过多；同时也给出了不正确的图案。

The following diagrams and tables show some examples recommended patterns to prevent excessive solder amounts; Examples of improper pattern designs are also shown.

电路板设计推荐图案尺寸：Recommended land dimensions for a typical chip capacitor land patterns for PCBs:

波峰焊接时推荐设计的尺寸(单位：mm)： Recommended land dimensions for Wave soldering (unit: mm):

规格 SIZE		0603	0805	1206
尺寸	L	1.6	2	3.2
	W	0.8	1.25	1.6
A		0.8~1.0	1.0~1.4	1.8~2.5
B		0.5~0.8	0.8~1.5	0.8~1.7
C		0.6~0.8	0.9~1.2	1.2~1.6



贴片电容
Chip Capacitor



回流焊接时推荐设计的尺寸(单位：mm)： Recommended land dimensions for Reflow soldering (unit: mm):

规格 SIZE		0603
尺寸 (mm)	L	1.6
	W	0.8
A		0.6~0.8
B		0.6~0.8
C		0.6~0.8

过量的焊锡会影响产品抵抗机械应力的能力，因此在设计图案时应引起注意。

Excess solder can affect the ability of chips to withstand mechanical stresses. Therefore, please take proper precautions when designing land patterns.

在应用中一些焊接好与坏的情况： Examples of good and bad solder application.

项目 Item	不推荐结构 Not recommended	推荐结构 Recommended
片状元件和带引线的元件的混合焊接 Mixed mounting of SMD and leaded component		
靠近底座的焊接 Component placement close to the chassis		
在片状元件附近带引线元件的焊接 Hand-soldering of leaded components near mounted components		

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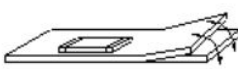
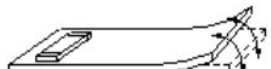
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Website <https://www.cctc.cc>

7.1.2 图案结构 Pattern configurations

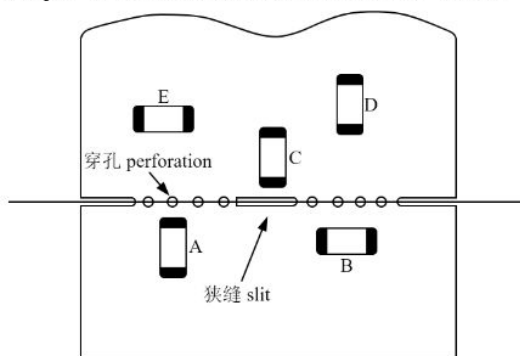
下面是电容器安装好与坏的例子。选择贴装位置应尽可能减小电路板在弯曲时受到的机械应力。

The following are examples of good and bad capacitor layout. SMD capacitors should be located to minimize any possible mechanical stresses from board warp or deflection.

项目 Item	不推荐结构 Not recommended	推荐结构 Recommended
电路板弯曲 Deflection of the board		

对于电路板分拨的电容器，在分拨时受到的机械应力大小与电容器的安装有关。下面推荐了一些好的设计。

To layout the capacitors for the breakaway PC board, it should be noted that the amount of mechanical stresses imposed depending on capacitor layout. The example below shows recommendations for better design.



应力大小顺序为：A > B = C > D > E
magnitude of stress: A > B = C > D > E

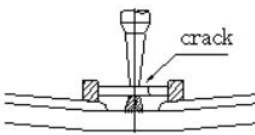
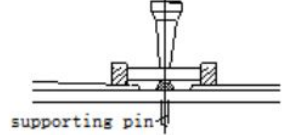
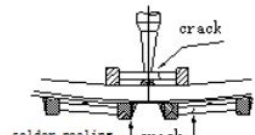
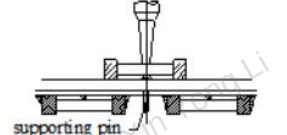
在沿着分拨线分拨电路板时，对产品施加的机械应力与使用的方法关系很大。分拨电路板时片状元件受到的疲劳按照如下顺序增大：分拨、剪切、V型槽、穿孔。因此，贴装时应该考虑电路板的分拨过程。

When breaking PC boards along their perforations, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: splitting, shearing, V-groove, and perforating. Thus, any ideal SMD capacitor layout must also consider the PCB splitting procedure.

7.2 自动贴装注意事项 Considerations for automatic placement

贴装机的调整 Adjustment of mounting machine

- ①. 产品在电路板贴装时，不应该受到过大的冲击。
- ②. 必须定期对吸头和定位爪进行检查、维修和更换。
- ①.Excessive impact load should not be imposed on the capacitors when mounting the PC boards.
- ②.The maintenance and inspection of the mounters should be conducted periodically.

项目 Item	不推荐结构 Not recommended	推荐结构 Recommended
单面贴装 Single-sided mounting		
双面贴装 Double-sided mounting		

7.3 推荐焊接曲线 Recommendedsolderingprofile

7.3.1 说明:

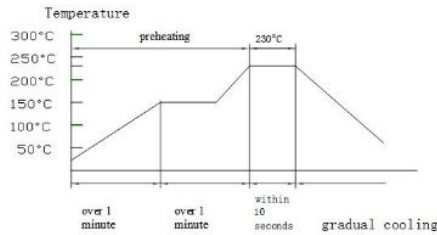
- ①. 产品推荐使用回流焊接工艺;
- ②. 大尺寸产品适用于回流焊接工艺。

Re:

- ①. Reflow soldering is recommended;
- ②. Reflow soldering is suitable for bigger size MLCCs.

7.3.2 锡铅焊接曲线 RecommendedSn&Pb solderingprofile

回流焊接 Reflowsoldering



注意 Caution

- ①. 理想状况的焊锡高度为电容器厚度的1/3~1/2，如下图所示：

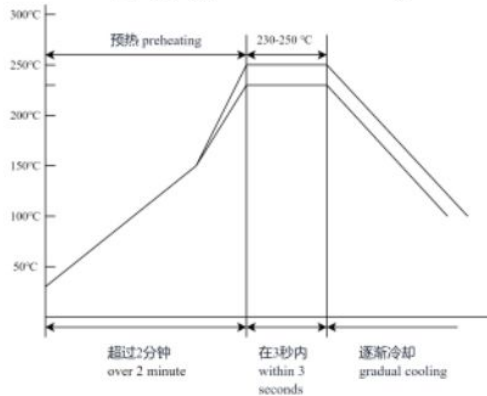
- ①. The ideal condition is to have solder mass (fillet) controlled to 1/2 to 1/3 of the thickness of the capacitor, as shown below:



- ②. 过长的焊接时间会影响端头的可焊性，焊接时间尽可能保持与推荐时间一致。

- ②. Because excessive dwell times can detrimentally affect solderability, soldering duration should be kept as close to recommended times as possible.

波峰焊接 Wave soldering



注意 Caution

- ①. 确保电容器充分预热。

- ②. 产品预热和焊接温度差不超过150℃。

- ③. 焊接后尽可能慢速冷却。

- ①. Make sure the capacitors are preheated sufficiently.

- ②. The temperature difference between product preheating and welding shall not exceed 150℃.

- ③. Cooling after soldering should be gradual as possible.

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手工焊接 Hand soldering

条件:

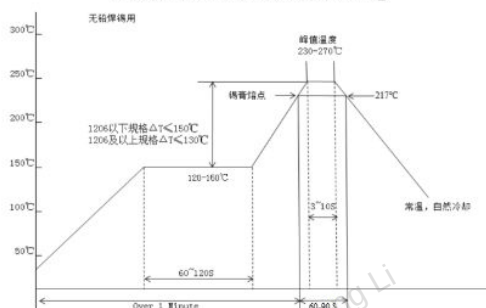
预热 Preheat	烙铁头温度 Soldering tip temperature	烙铁功率 Soldering power	烙铁头直径 Diameter of soldering iron head	焊接时间 Welding time	锡容量 Solder paste content	限制条件 Restriction condition
$\Delta \leq 130^{\circ}\text{C}$	最高350℃ Up to 350℃	最大20W Max. 20W	建议1mm Recommended 1mm	最长5s Up to 5s	$\leq 1/2$ 芯片厚度 $\leq 1/2$ chip thickness	请勿使用烙铁头直接接触陶瓷原件 Do not use the soldering tip to touch the ceramic element directly

注意 Caution

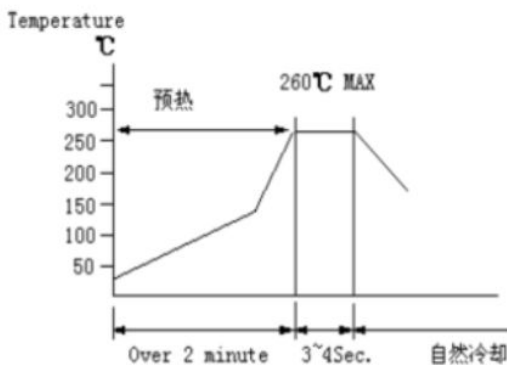
- ①. 用尖端最大直径1.0mm功率20W的焊接烙铁。
- ②. 焊接烙铁不要直接接触产品。
- ①. Use a 20W soldering iron with a maximum tip diameter of 1.0mm.
- ②. The soldering iron should not directly touch the capacitor.

7.3.3 无铅焊接曲线 Recommended Pb-Free soldering profile

回流焊接 Reflow soldering



波峰焊接 Wave soldering



波峰焊/回流焊的容值范围 Capacitance range of Wave soldering and Reflow soldering:

焊接模式 Welding mode	0402及以下 0402 and below	0603	0805	1206	1210及以上 1210 and above
波峰焊 Wave soldering	/	105及以下容值 Capacitance of 105 and below	225及以下容值 Capacitance of 225 and below	475及以下容值 Capacitance of 475 and below	/
回流焊 Reflow soldering	所有规格 All Specifications	所有规格 All Specifications	所有规格 All Specifications	所有规格 All Specifications	所有规格 All Specifications



7.4 分拨电路板 Handling breakaway PC boards (splitting along perforation)

- ①. 在电容器或其它贴装后，必须注意因电路板弯曲或变形带来的应力。
- ②. 分拨电路板时必须使用专用的夹具，不可以用手扳断。
- ①. When splitting the PC board after mounting capacitors and other components, care is required so as not to Subject the board to excessive bending or torsional stress.
- ②. Board separation should not be done manually, but by using the appropriate fixture.

7.5 保存 Storage

- ①. 在下列环境中保存产品：温度5~40℃；湿度≤70%RH。
- ②. 产品自生产之日保存期为一年，产品使用之前请勿拆开编带；
- ③. 编带拆开后，产品应在三个月内使用。
- ④. 高介电常数电容器（X7R/X7S/X7T/X6S/X5R）的容值随时间会逐渐减小，所以在电路设计时应充分考虑这一现象。容值减小的电容器在150℃热处理1小时后容值会恢复到初始值。
- ①. Keep the storage environment conditions as following Temperature: 5~40℃; Humidity: ≤70% RH
- ②. Don't open the tape until the parts are to be used, and store them within one year since the date printed on the reel;
- ③. Use the chips within 3 months after the tape is opened.
- ④. The capacitance value of high dielectric constant capacitors (X7R/X7S/X7T/X6S/X5R) will gradually decrease with the passage of time, so this should be taken into consideration in the circuit design. If such a capacitance reduction occurs, a heat treatment of 150℃ for 1 hour will return the capacitance to its initial level.

7.6 环保申明 Environmental Declaration

- ①. 我司所有MLCC产品均符合RoHS 2.0标准；
- ②. 我司所有MLCC产品均符合最新的REACH法规要求；
- ③. 我司所有MLCC产品均符合HF要求。
- ①. All MLCC products of our comply with RoHS 2.0;
- ②. All MLCC products of our comply with the latest REACH regulations;
- ③. All MLCC products of our comply meet HF requirements.